

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10002217
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01207M

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/JP/	1	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000); 5 pages	<input type="checkbox"/>
/JP/	2	ANONYMOUS, "Warp Nine Engineering - The IEEE 1284 Experts-F/Port Product Sheet," undated web page found at http://www.fapo.com/fport.htm ; 2 pages printed on 4/12/2005	<input type="checkbox"/>
/JP/	3	ANONYMOUS, "F/Port:Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm ; 25 pages	<input type="checkbox"/>
/JP/	4	Nam et al.; "Fast Development of Source-Level Debugging System Using Hardware Emulation"; IEEE 2000; 4 pages	<input type="checkbox"/>
/JP/	5	Huang et al.; "Iceberg: An Embedded In-Circuit Emulator Synthesizer For Microcontrollers"; ACM 1999. 6 pages	<input type="checkbox"/>
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/JP/	8	Hong et al.; "An FPGA-Based Hardware Emulator for Fast Fault Emulation"; IEEE 1997; 4 pages	<input type="checkbox"/>
/JP/	9	Ching et al.; "An In-Circuit-Emulator for TMS320C25"; IEEE 1994; 6 pages	<input type="checkbox"/>
/JP/	10	Pasternak; "In-Circuit-Emulation in ASIC Architecture Cor Designs"; IEEE 1989; 4 pages	<input type="checkbox"/>
/JP/	11	Melear. "Using Background Modes for Testing, Debugging and Emulation of Microcontrollers"; IEEE 1997; 8 pages	<input type="checkbox"/>

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/JP/	12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	<input type="checkbox"/>
/JP/	13	Anonymous; "JEEN JTAG Embedded Ice Ethernet Interface"; August 1999; Embedded Performance, Inc.; 3 pages	<input type="checkbox"/>
/JP/	14	Sedory; "A Guide to Debug"; 2004; retrieved on May 20, 2005; 7 pages	<input type="checkbox"/>
/JP/	15	"Microsoft Files Summary Judgement Motions"; February 1999; Microsoft PressPass; retrieved on May 20, 2005 from http://www.microsoft.com/presspass/press/1999/feb99/Feb99/Calderapr.asp ; 3 pages	<input type="checkbox"/>
/JP/	16	Xerox; "Mesa Debugger Documentation"; April 1979; Xerox Systems Development Department, Version 5.0; 33 pages	<input type="checkbox"/>
/JP/	17	Stallman et al.; "Debugging with the GNU Source-Level Debugger", January 1994; retrieved on May 2, 2005 from http://www.cs.utah.edu ; 4 pages	<input type="checkbox"/>
/JP/	18	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007 ; 4 pages 11/8/2007	<input type="checkbox"/>
/JP/	19	Stan Augarten; "The Chip Collection - Introduction - Smithsonian Institute", "State of the Art", "The First 256-Bit Static RAM"; retrieved on November 14, 2005 from http://smithsonianchips.si.edu/augarten/p24.htm ; 2 pages	<input type="checkbox"/>
/JP/	20	"POD - Piece of Data, Plain Old Documentation, Plain Old Dos..."; retrieved on November 14, 2005 from http://www.auditmypc.com/acronym/POD.asp ; 2 pages	<input type="checkbox"/>
/JP/	21	"Pod-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from http://en.wikipedia.org/wiki/Pod ; 3 pages 11/8/2007	<input type="checkbox"/>
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/JP/	23	"In-Circuit Emulators - descriptions of the major ICEs around"; retrieved on November 11, 2005 from http://www.algonet.se/~staffann/developer/emulator.htm ; 6 pages 11/8/2007	<input type="checkbox"/>
/JP/	24	CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation," October 10, 2001; 09/975,104; Snyder; 35 pages	<input type="checkbox"/>
/JP/	25	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
/JP/	26	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools"; Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	<input type="checkbox"/>
/JP/	27	multiple-user system to Microsoft Press Computer User's Dictionary; 1998; 3 pages multisync monitor	<input type="checkbox"/>
/JP/	28	CD00186; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events;" October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	<input type="checkbox"/>
/JP/	29	CD00185; "Emulator Chip-Board Architecture for Interface;" October 10, 2001; 09/975,030; Snyder et al.; 37 pages	<input type="checkbox"/>
/JP/	30	Wikipedia - Main Page, retrieved on March 8, 2006 from http://en.wikipedia.org/wiki/Main_Page and http://en.wikipedia.org/wiki/Wikipedia:Introduction ; 5 pages	<input type="checkbox"/>
/JP/	31	Wikipedia - Processor register, retrieved on March 7, 2006 from http://en.wikipedia.org/wiki/Processor_register ; 4 pages	<input type="checkbox"/>
/JP/	32	Dahl, et al.; "Emulation of the Sparcle Microprocessor with the MIT Virtual Wires Emulation System"; 1994; IEEE; 9 pages	<input type="checkbox"/>
/JP/	33	Bauer et al., "A Reconfigurable Logic Machine for Fast Event-Driven Simulation"; 1998; Design Automation Conference Proceedings; 8 pages	<input type="checkbox"/>

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/JP/	34	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 250 pages 264 pages	<input type="checkbox"/>
/JP/	35	USPTO Final Rejection for Application Number 10/001,478 (CD01213M) dated 04/20/2009; 16 pages	<input type="checkbox"/>
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